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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/778,466	02/07/2001	Mark Phillips	S1022/8618	5904

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EXAMINER
MCCARTHY, CHRISTOPHER S

ART UNIT	PAPER NUMBER
2113	

DATE MAILED: 01/05/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 10/03)

	Application No.	Applicant(s)
Office Action Summary	09/778,466	PHILLIPS, MARK
	Examiner	Art Unit	
	Christopher S. McCarthy	2113	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE *3* MONTH(S) FROM
THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed
  after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any
  earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *07 February 2001*.

2a)☐ This action is **FINAL**.　　　2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is
closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-13* is/are pending in the application.

　　4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1,2,9 and 10* is/are rejected.

7)☒ Claim(s) *3-8 and 11-13* is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☒ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *07 February 2001* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

　　Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

　　Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. §§ 119 and 120

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

　　a)☒ All b)☐ Some * c)☐ None of:

　　　1.☒ Certified copies of the priority documents have been received.

　　　2.☐ Certified copies of the priority documents have been received in Application No. _____.

　　　3.☐ Copies of the certified copies of the priority documents have been received in this National Stage
　　　　application from the International Bureau (PCT Rule 17.2(a)).

　　* See the attached detailed Office action for a list of the certified copies not received.

13)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application)
since a specific reference was included in the first sentence of the specification or in an Application Data Sheet.
37 CFR 1.78.

　　a) ☐ The translation of the foreign language provisional application has been received.

14)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific
reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)　　　　　　4) ☐ Interview Summary (PTO-413) Paper No(s). _____ .

2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)　　5) ☐ Notice of Informal Patent Application (PTO-152)

3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) *4,5* .　　6) ☐ Other: .

DETAILED ACTION

Specification

1.   The title of the invention is not descriptive. A new title is required that is clearly

indicative of the invention to which the claims are directed.

Claim Rejections - 35 USC § 102

2.   The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the

basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed
> in the United States before the invention by the applicant for patent or (2) a patent granted on an application for
> patent by another filed in the United States before the invention by the applicant for patent, except that an
> international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this
> subsection of an application filed in the United States only if the international application designated the United
> States and was published under Article 21(2) of such treaty in the English language.

3.   Claims 1-2, 9-10 are rejected under 35 U.S.C. 102(e) as being anticipated by Pettus U.S.

Patent 6,360,266.

As per claim 1, Pettus teaches a method of debugging a target system using a host system

connected thereto (column 8, lines 28-37), the target system comprising a digital signal processor

having associated memory comprising plural addressable locations (column 5, lines 25-38), said

target system further having a reserved storage location designated as a vector (column 4, lines

38-44), said memory further storing plural application programs, each application program

having respective associated exception handler code (column 4, lines 38-44; column 11, lines 5-

11), the method comprising: dynamically loading an stack to said reserved region (column 4,

lines 38-44; column 10, lines 36-50); causing the vector of said target system to point to said

stack whereby all said applications use the said stack for said exception (column 4, lines 42-44;

column 11, lines 5-11).

As per claim 2, Pettus teaches the method of claim I further comprising the steps of:

dynamically loading a library to said target from said host whereby said dynamically loaded

library has an entry point at one of said plural addressable locations, wherein said library

includes at least one routine needed for running at least one of said applications; and storing

information indicative of the address of said one location at a reserved location in said stack

(column 14, lines 3-67).

As per claim 9, Pettus teaches a device for debugging a target system, the device

comprising a host system connected thereto (column 8, lines 28-37), the target system

comprising a digital signal processor having associated memory comprising plural addressable

locations (column 5, lines 25-38), said target system further having a reserved storage location

designated as a vector (column 4, lines 38-44), said memory further storing plural application

programs, each application program having respective associated exception handler code

(column 4, lines 38-44; column 11, lines 5-11), the device further comprising: stack dynamic

loading circuitry in said host for dynamically loading a stack to said reserved region, whereby

said loader comprises an indication of the location in said memory of said stack (column 4, lines

38-44; column 10, lines 36-50); vector writing circuitry receiving said indication, and writing to

said the vector of said target system the address of said stack whereby all said applications use

the said stack for said exception (column 4, lines 42-44; column 11, lines 5-11).

As per claim 10, Pettus teaches the device of claim 9 further comprising: a computer file

in said host, said file comprising a library having a routine needed by at least one of said

applications; library dynamic loading circuitry for dynamically loading said library to said target

from said host whereby said dynamically loaded library has an entry point at one of said plural

addressable locations; and stack writing circuitry for storing information indicative of the address

of said one location at a reserved location in said stack (column 14, lines 3-67).

Allowable Subject Matter

4.   Claims 3-8, 11-13 are objected to as being dependent upon a rejected base claim, but

would be allowable if rewritten in independent form including all of the limitations of the base

claim and any intervening claims.

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Christopher S. McCarthy whose telephone number is (703)305-

7599. The examiner can normally be reached on M-F, 8 - 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Robert Beausoliel can be reached on (703)305-9713. The fax phone number for the

organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is (703)305-3900.

csm
December 24, 2003

ROBERT BEAUSOLIEL
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100